



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	10/767,751	Confirmation No.	5740
First Inventor	Richard K. Williams	Filing Date	01/28/2004
Tech. Center/ Art Unit	2812	Examiner	Matthew E. Warren
Title:	Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Technology		
Docket No.:	AAT007-4C US	Customer No.:	34036

Mail Stop Issue Fee
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Santa Clara, California
May 18, 2006

*Okay to
Enter Amt.
grew 7/6/06*

AMENDMENT AFTER ALLOWANCE

Dear Sir:

Pursuant to 37 CFR 1.312, please amend the above-identified application as follows.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 3 of this paper and include both attached replacement sheets and annotated sheets showing changes.

Remarks/Arguments begin on page 4 of this paper.

An **Appendix** including amended drawing figures is attached following page 4 of this paper.

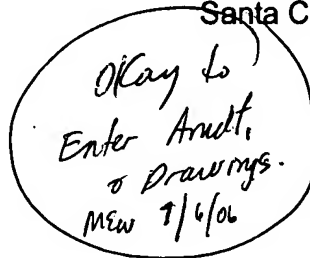


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COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Santa Clara, California
May 25, 2006



RESPONSE TO NOTICE REGARDING DRAWINGS

Dear Sir:

In response to the Notice Regarding Drawings dated May 9, 2006, please amend the above-identified application as follows.

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.